

[illegible]

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Figures

Figure 1: A schematic diagram illustrating the experimental setup for measuring the time delay of a signal. The diagram shows a signal source (S) connected to a delay line (DL) and a detector (D). The signal source is represented by a square wave, and the delay line is represented by a series of rectangular blocks. The detector is represented by a single rectangular block. The signal path is indicated by arrows, showing the signal traveling from the source through the delay line to the detector. The time delay is labeled as Δt .